WEST Refine Search Page 1 of 2

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Term	Documents
(10 AND 23).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	22
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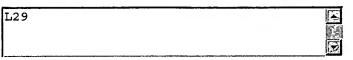
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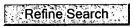
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DB=P	GPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR = YES; OP = SPAR	:OR	
<u>L29</u>	L23 and 110	22	<u>L29</u>
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<u>L27</u>	L23 and 18	102	<u>L27</u>
<u>L26</u>	L23 and 17	602	<u>L26</u>
<u>L25</u>	L23 and 16	872	<u>L25</u>
<u>L24</u>	L23 and 15	872	<u>L24</u>
<u>L23</u>	112 and (maxium or max or longest)	1510	<u>L23</u>
<u>L22</u>	112 and 110	24	<u>L22</u>
<u>L21</u>	112 and 19	212	<u>L21</u>
<u>L20</u>	112 and 18	150	<u>L20</u>
<u>L19</u>	112 and 17	863	<u>L19</u>
<u>L18</u>	112 and 15	1247	<u>L18</u>
<u>L17</u>	111 and 110	94	<u>L17</u>

Page 2 of 2

<u>L16</u>	l11 and l9	262	<u>L16</u>	
<u>L15</u>	111 and 18	319	<u>L15</u>	
<u>L14</u>	111 and 17	1151	<u>L14</u>	
<u>L13</u>	111 and 15	1826	<u>L13</u>	
<u>L12</u>	L11 and l4	2125	<u>L12</u>	
<u>L11</u>	12 and 13	10568	<u>L11</u>	
DB=P	GPB, USPT; PLUR=YES; OP=OR			
<u>L10</u>	(717/140-145,159)[CCLS]	1798	<u>L10</u>	
<u>L9</u>	(712/210-213)[CCLS]	910	<u>L9</u>	
<u>L8</u>	(711/202-221)[CCLS]	6301	<u>L8</u>	
<u>L7</u>	(712/205-230,300)[CCLS]	7168	<u>L7</u>	
<u>L6</u>	(712/2-300)![CCLS]	12964	<u>L6</u>	
<u>L5</u>	(712/2-300)[CCLS]	12964	<u>L5</u>	
DB=PGPB, $USPT$, $USOC$, $EPAB$, $JPAB$, $DWPI$, $TDBD$; $PLUR=YES$; $OP=OR$				
<u>L4</u>	(fetch\$5 or prefetch\$5 or issu\$4 or dispatch\$5) near25 decod\$3	20481	<u>L4</u>	
<u>L3</u>	(concurrent\$5 or parallel\$5 or simultaneous\$4) near6 execut\$5	65036	<u>L3</u>	
<u>L2</u>	(variable or different or adjust\$6) near6 (length or width or size)	1106142	<u>L2</u>	
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IEEE STD IEEE Standard	Yi-Ping You; Jenq Kuen Lee; Embedded and Real-Time Computing Systems and Applications, 2006, Proceedings, 12th IEEE In on
	16-18 Aug. 2006 Page(s):215 - 222 Digital Object Identifier 10.1109/RTCSA.2006.40
·	AbstractPlus Full Text: PDF(600 KB) IEEE CNF Rights and Permissions
	 Dictionary based code compression for variable length instruction encodings Das, D., Kumar, R.; Chakrabarti, P.P.; VLSI Design, 2005, 18th International Conference on 3-7 Jan. 2005 Page(s):545 - 550 Digital Object Identifier 10.1109/ICVD.2005.81
	AbstractPlus Full Text: PDF(128 KB) IEEE CNF Rights and Permissions
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Martin, D.; Owen, R.E.;

Acoustics, Speech, and Signal Processing, 1998, ICASSP '98, Proceedings of the 1998 IEEE Inter

Volume 5, 12-15 May 1998 Page(s):3097 - 3100 vol.5 Digital Object Identifier 10.1109/ICASSP.1998.678181 AbstractPlus | Full Text: PDF(388 KB) IEEE CNF Rights and Permissions Real-time MPEG-2 software decoding with a dual-issue RISC processor Holmann, E.; Yamada, A.; Yoshida, T.; Uramoto, S.; VLSI Signal Processing, IX, 1996., [Workshop on] 30 Oct.-1 Nov. 1996 Page(s):105 - 114 Digital Object Identifier 10.1109/VLSISP.1996.558309 AbstractPlus | Full Text: PDF(672 KB) IEEE CNF Rights and Permissions 7. A real-time digital VCR encode/decode and MPEG-2 decode LSI implemented on a dual-issu Mohri, A.; Yamada, A.; Yoshida, Y.; Sato, H.; Takata, H.; Nakakimura, K.; Hashizume, M.; Shimots K.; Solid-State Circuits, IEEE Journal of Volume 34, Issue 7, July 1999 Page(s):992 - 1000 Digital Object Identifier 10.1109/4.772415 AbstractPlus | References | Full Text: PDF(328 KB) | IEEE JNL Rights and Permissions 8. Scalar program performance on multiple-instruction-issue processors with a limited number Mahlke, S.A.; Chen, W.Y.; Chang, P.P.; Hwu, W.W.; System Sciences, 1992, Proceedings of the Twenty-Fifth Hawaii International Conference on Volume i, 7-10 Jan. 1992 Page(s):34 - 44 vol.1 Digital Object Identifier 10.1109/HICSS.1992.183141 AbstractPlus | Full Text: PDF(612 KB) IEEE CNF

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